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01/03/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM 10038264	FILING DATE 01/03/2002	CLASS 403	SUBCLASS 270	GAU 3679	EXAMINER GARCIA
**APPLICANTS: Park Seungbae; Sathe Sanjeev; Zubelewicz Aleksander;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/430,965 11/01/1999 PAT 6,347,901					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				EN999048D	
TITLE : Solder interconnect techniques					

U.S. DEPT. OF COMMERCE/PAT. & TRADEMARK OFFICE

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Applicant Examiner	
		PREPARED FOR ISSUE	
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